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A Reconfigurable Analog VLSI Neural Network Chip 765 Organization of the Chip Figure 9 shows how the distributed-neuron synapses are arranged on-chip. 16 distributed-neuron synapses have been arranged in a 4 x 4 crossbar fashion to form a 4-input-4-output network. We call this a '4 x 4 tile'. Input and output

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